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Si2 Announces New Session for "Si2 Roundup@DAC"

Design Automation Conference 2012

AUSTIN, Texas--(BUSINESS WIRE)--The Silicon Integration Initiative (Si2) today announced a new session for the "Si2 Roundup@DAC: Standards in Action" co-located event at the Design Automation Conference, which will be held June 3-7 at The Moscone Center in San Francisco, CA. The new session entitled "New Si2 Standards In Action On Real-World Tools" will be on Monday, June 4, from 2PM – 3PM, in Room 301.

This session will demonstrate the recently published Si2 Open Process Design Kit (OpenPDK) standards and Open Design for Manufacturability (OpenDFM) standards working with commercially available tools. Si2 standards promote interoperability and these demos are designed to illustrate that point. The demos will show the OpenPDK symbol standard validating the correctness or incorrectness of a schematic symbol change as compared to the standard in real time. The OpenDFM standard will demonstrate an integrated physical verification flow from an electronic Design Rule Manual (eDRM) to XML, to OpenDFM and to four different DRC engines.

> **"New Si2 Standards In Action On Real-World Tools"**

This new session is part of a free day-long program, consisting of 4 individual events, which will showcase activities currently underway in order to demonstrate the value of these programs to the semiconductor industry. Attendees should include engineers and technologists working at both current and cutting-edge technology nodes and also managers responsible for driving both design and manufacturing strategy, and related financial and staffing decisions.

All sessions are summarized in this link: http://www.si2.org/?page=1525.

All sessions are free, register here: http://www.si2.org/events_register/dac_2012_registration.php.

For admission into the Moscone Center you will need to register for DAC at this link:

http://www.dac.com/registration+rates.aspx.

Note that there is a Free Monday option, which will be sufficient to get into the convention hall and then to Room 301.

Several of these events are sponsored by Cadence Design Systems, GLOBALFOUNDRIES, LSI, NanGate, and Spectral Design & Test.

About Si2

Si2 is the largest organization of industry-leading semiconductor, systems, EDA and manufacturing companies focused on the development and adoption of standards to improve the way integrated circuits are designed and manufactured, in order to speed time-to market, reduce costs, and meet the challenges of sub-micron design. Now in its 24th year, Si2 is uniquely positioned to enable timely collaboration through dedicated staff and a strong implementation focus driven by its member companies. Si2 represents nearly 100 companies involved in all parts of the silicon supply chain throughout the world. See www.si2.org.

Contacts

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