



January 15, 2007 10:00 AM Central Time

Si2 Open Modeling Coalition Gains Access to Statistical Library Characterization Tool from Altos Design Automation

AUSTIN, Texas--(BUSINESS WIRE)--Altos Design Automation today made available Variety[™], the newly released statistical library characterization tool, to the Open Modeling Coalition (OMC) of the Silicon Integration Initiative (Si2).

OMC is creating a reference flow for library characterization, modeling, and model usage. Variety will be installed into this reference flow as the characterization subsystem for statistical characterization of library data. As part of this reference flow, Variety and Altos will help the OMC define standards for library flow interfaces for communicating statistical information between elements in the flow.

Variety[™] addresses the critical variation aware characterization and statistical signoff aspects of library IP. This next-generation characterization tool will be used in the validation of reference flows for emerging standards for library IP. Cell library format convergence and predictable data exchange have become the mandate from user companies across the semiconductor industry.

“The rapid and accurate process variation aware library characterization is essential to removing a major bottleneck in the adoption of statistical design,” says Jim McCanny, CEO of Altos Design Automation. “The OMC, through its SSTA Working Group, is working to define and deliver standards that allow emerging SSTA tools to collaborate throughout the design process. Altos will assist in the validation and refinement of OMC standards as part of the OMC reference flow.”

“Statistical timing is rapidly emerging as an important new methodology for leading-edge silicon design,” said Steve Schulz, president and CEO of Si2. “Having the support of Altos and their characterization software as part of OMC reference flows will make possible broad adoption of statistical timing techniques through the widespread acceptance of much-needed enabling industry standards.”

About Si2

Si2 is an organization of industry-leading semiconductor, systems, EDA, and manufacturing companies focused on improving the way integrated circuits are designed and manufactured in order to speed time-to-market, reduce costs, and meet the challenges of sub-micro design. Si2 is uniquely positioned to enable collaboration through a strong implementation focus driven by its member companies. Si2 focuses on developing practical technology solutions to industry challenges. Si2 represents over 100 companies involved in all parts of the silicon supply chain throughout the world.

About Altos Design Automation

Altos Design Automation provides ultra-fast, fully-automated characterization technology for the creation of library views for timing, signal integrity and power analysis and optimization. Altos advanced modeling solutions are used by both corner-based and statistical-based design implementation flows to reduce time to market and improve yield.

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